## **Inventor Information**

Inventor One Given Name:: Ashley Family Name:: Saulsbury

Name Suffix::

Postal Address Line One:: 18488 Grizzly Rock Road

City:: Los Gatos

State or Province:: CA
Postal or Zip Code:: 95033

Citizenship Country:: United Kingdom

Inventor Two Given Name:: Nyles Family Name:: Nettleton

Family Name:: Name Suffix::

Postal Address Line One:: 1368 Hacienda Court

City:: Campbell

State or Province:: CA
Postal or Zip Code:: 95008
Citizenship Country:: US

Inventor Three Given Name:: Michael Family Name:: Parkin

Family Name:: Name Suffix::

Postal Address Line One:: 4277 Mackay Drive

City:: Palo Alto

State or Province:: CA
Postal or Zip Code:: 94306
Citizenship Country:: US

Inventor Four Given Name:: David Family Name:: Emberson

Name Suffix::

Postal Address Line One:: 300 Moore Creek Road

City:: Santa Cruz

State or Province:: CA
Postal or Zip Code:: 95060
Citizenship Country:: US

**Correspondence Information** 

Correspondence Customer Number:: 20350

**Application Information** 

Title Line One:: VLIW Computer Processing Architecture with On-chip DRAM Usable as Physical

Title Line Three:: Memory or Cache Memory

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Formal Drawings?:: No

Application Type:: Docket Number::

Secrecy Order in Patent Appl.?::

Utility

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No

## **Continuity Information**

This application is a:: > Application One:: Filing Date::

Patent Number::

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